Address	Function Name	Function Description
0x0000	CCD_CDSREG	Writing to this register loads data to the addressed CDS channel
~~0x0001~~	~~CCD_DOPREG~~	~~Digital Output Port; a 32 bit medium speed port~~
0x0003	CCD_SETIDENT	Sets a mode that allows individual ADC channels to identify themselves by synthetic data generation
0x0004	CCD_BURST	Command to burst send pixel data. Alternative to Pipeline write mode
0x0005	CCD_SEQTRIG	Triggers the on board micro-sequencer to sequence the CDS switches during pixel acquisition
0x0100 => 0x010B	CCD_ADCDATA	Twelve (12) read only registers that reflect raw ADC data
0x0200 => 0x020B	CCD_ADCCFG	Twelve (12) configuration registers to establish the ADC mode of operation
0x0210 => 0x021B	CCD_VOFFSETS	Access to twelve (12) DC offset DACs used to bring the ADC signal into the converters dynamic range
0x0220 => 0x024F	CCD_HVBIASES	Access to fourty-eight (48) voltage DACs to set the high voltage bias signal values
0x0250 => 0x027F	CCD_HVBIASTELMODE	Fourty-eight (48) configuration registers to establish the operating mode of the telemetry ADCs
0x0280	CCD_AUXCFGREG	Bit register to control auxiliary functions of the board
0x0300 => 0x030E	CCD_REDIRECT	15 indirection registers to map signal channels to the order of pixel data during acquisition
0x030F	CCD_XFERCOUNT	Register to control the number of signal channels to acquire during acquisition
0x0310		ADC Count Register
0x0440 => 0x047F	CCD_BIASTELEMETRY	Fourty-eight (48) high voltage bias telemetry values
0x0480 => 0x0483	DB_REG	Debug register
0x1000 => 0x103F	CCD_PAT_MEM	Access to the micro-sequencer pattern and timing memory space
0xC000=> 0xC002	CCD_AUDIT_REG	Audit register that counts the number of ADC convert strobes after an exposure start command
0xFFF0	CCD_EVENT_REG	Global event register used to receive event strobes
0xFFF8	VSUB_ON_OFF	Turns Vsub Voltage on if any bit is set, Off if zero
0xFFF9	CCD_LEDCTL	Front panel LED indicator configuration register
0xFFFA	CCD_SERNUM	Electronic serial number read only register
0xFFFB	CCD_TEMP	Access to the local board temperature sensor
0xFFFC	CCD_CTLREG	Bit register used to control functionality of the board
0xFFFD	CCD_STATUSREG	Board status register
0xFFFE	CCD_IDENTREG	Board function identity register + shadow reset
0xFFFF	CCD_FIRMVERS	Board firmware version register + shadow reboot
0xFFFE	CCD_RESET	Writing to this location causes a soft reset of the board. Does not affect configuration data
0xFFFF	CCD_REBOOT	Writing to this location causes a hard reset (reboot) of the board with default configuration